

2113

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/074,174	
	Filing Date	02/10/02	
	First Named Inventor	Haw-minn Lu	
	Art Unit	2113	
	Examiner Name	Robert W. Beausoliel, Jr.	
Total Number of Pages in This Submission	4	Attorney Docket Number	

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please Identify below): USPTO Form SB/08B (1pg); Cited References (1 reference total); Return Postcard
<div>Remarks</div> <p>Information Disclosure Statement is submitted herewith as 2 pages.</p>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Haw-minn Lu
Signature	
Date	December 4, 2004

CERTIFICATE OF TRANSMISSION/MAILING			
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.			
Typed or printed name	Haw-minn Lu		
Signature		Date	December 4, 2004

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In the United States Patent and Trademark Office

Serial Number: 10/074,174
Application. Filed: 02/10/02
Applicant(s): Haw-minn Lu
Application. Title: Width Upgrade for a Scalable Switching Network
Examiner: Robert W. Beausoliel, Jr.
Group Art Unit: 2113

Mailed: 2004 December 4
At: San Diego, CA

Information Disclosure Statement

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

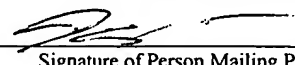
In compliance with the Applicant's duty under 37 CFR 1.97-98, the following information is brought to the attention of the Examiner. The items are listed on the attached Form PTO/SB/08B and copies of each are enclosed for the convenience of the Examiner. The Applicant respectfully requests that the documents be made of record in the above-referenced application.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with anything referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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Haw-minn Lu
Name of Person Mailing Paper


Signature of Person Mailing Paper

The items identified in this Information Disclosure Statement were cited by Applicant in response filed on October 4, 2004 to an office action mailed on May 3, 2004 by examiner Derrick W. Ferris directed towards U.S. Patent Application No. 09/897,263, filed on July 2, 2001 by the Applicant.

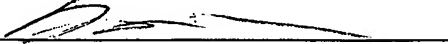
The items identified in this Information Disclosure Statement may or may not be "material" pursuant to 37 CFR 1.56. The submission of this document thereof by the Applicant shall not be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. § 102 with respect to this invention unless specifically designated by the Applicant as such.

The filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information, as defined in 37 CFR 1.56, exists.

The attached IDS is being filed in accordance with 37 CFR §§ 1.97 and 1.98. This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b)(3), that is before the mailing of a first Office Action on the merits. Thus, no petition or fee is required.

Respectfully submitted,

Dated: December 4, 2004

By: 
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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/074,174
Filing Date	02/10/02
First Named Inventor	Haw-minn Lu
Art Unit	2113
Examiner Name	Robert W. Beausoliel, Jr
Attorney Docket Number	

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		AGRAWAL, "Testing and Fault-Tolerance of Multistage Interconnection Networks," Computer, Apr. 1982, pp. 41-53, vol. 15, no. 4, IEEE, US	
		BHUYAN, et. al "Design and Performance of Generalized Interconnection Networks." IEEE Transactions on Computers, Dec. 1983, pp. 1081-1090, vol. 32, no. 12, IEEE, US	
		BLAKE, et. al "Multistage Interconnection Network Reliability," IEEE Transactions on Computers, Nov. 1989, pp. 1600-1603, vol. 38, no. 11, IEEE, US	
		CHIN, et. al "Packet Switching Networks for Multiprocessors and Data Flow Computers," IEEE Transactions on Computers, Nov. 1984, pp. 991-1003, vol. 33, no. 11, IEEE, US	
		KUMAR, et. al "Failure Dependent Performance Analysis of a Fault-Tolerant Multistage Interconnection Network," IEEE Transactions on Computers, Dec. 1989, pp. 1703-1713, vol. 38, no. 12, IEEE, US	
		TZENG, et. al "Realizing Fault-Tolerant Interconnection Network via Chaining," IEEE Transactions on Computers, Apr. 1988, pp. 458-462, vol. 37, no. 4, IEEE, US	
		VARMA, et. al "Fault-Tolerant Routing in Multistage Interconnection Networks," IEEE Transactions on Computers, Mar. 1989, pp. 385-393, vol. 38, no. 3, IEEE, US	

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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